

Friday, November 20, 2009

OU hosted Half Day Design Workshop with XILINX Spartan 3 E – FPGA

Half day Free Workshop at Room DHE 147, 9 AM to 12 noon, Friday November 20, 2009

Design with Xilinx Spartan 3 E – FPGA by Kevin Klopfenstein, Xilinx.

Demo and Hands on workshop on Spartan 3 E starter Kit and EDK- embedded Development Kit 11.3.
Implementation of MicroBlaze™ soft processor delivers inexpensive, highly functional embedded processing.

- Industry's most versatile, low-cost **embedded processing** platform
- Integrate processor into FPGA and reduce BOM
- Reduce obsolescence risks with soft processors
- Reduce inventory cost by using common flexible embedded processing architecture across multiple products

Certificate of "Recognition of Attendance" for attending the workshop will be provided.

All are welcome. Seating is limited. Pre-registration is a must. Pre-register by sending a email to: Prof. Subra Ganesan: ganesan@oakland.edu.

Workshop Sponsored by IEEE Student Chapter at Oakland University, IEEE SEM Computer chapter, and ECE department, Oakland University.

SUMMARY

Created by Ashok Prajapati (akprajap@oakland.edu) on Friday, November 27, 2009
Modified by Ashok Prajapati (akprajap@oakland.edu) on Friday, November 27, 2009
Article Start Date: Friday, November 20, 2009